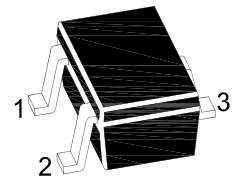
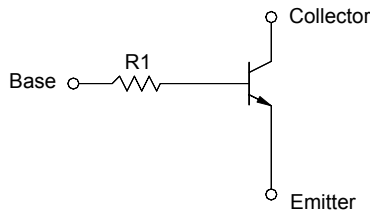


MMDTC114TE

NPN Silicon Epitaxial Planar Digital Transistor

Features

- With built-in bias resistors
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process



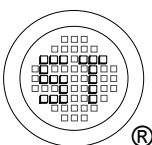
1.Base 2.Emitter 3.Collector
SOT-523 Plastic Package

Absolute Maximum Ratings ($T_a = 25\text{ }^\circ\text{C}$)

Parameter	Symbol	Value	Unit
Collector Base Voltage	V_{CBO}	50	V
Collector Emitter Voltage	V_{CEO}	50	V
Emitter Base Voltage	V_{EBO}	5	V
Collector Current	I_C	100	mA
Power Dissipation	P_{tot}	150	mW
Junction Temperature	T_j	150	$^\circ\text{C}$
Storage Temperature Range	T_{stg}	- 55 to + 150	$^\circ\text{C}$

Characteristics at $T_a = 25\text{ }^\circ\text{C}$

Parameter	Symbol	Min.	Typ.	Max.	Unit
DC Current Gain at $V_{CE} = 5\text{ V}$, $I_C = 1\text{ mA}$	h_{FE}	100	-	600	-
Collector Base Cutoff Current at $V_{CB} = 50\text{ V}$	I_{CBO}	-	-	500	nA
Emitter Base Cutoff Current at $V_{EB} = 4\text{ V}$	I_{EBO}	-	-	500	nA
Collector Base Breakdown Voltage at $I_C = 50\text{ }\mu\text{A}$	$V_{BR(CBO)}$	50	-	-	V
Collector Emitter Breakdown Voltage at $I_C = 1\text{ mA}$	$V_{(BR)CEO}$	50	-	-	V
Emitter Base Breakdown Voltage at $I_E = 50\text{ }\mu\text{A}$	$V_{BR(EBO)}$	5	-	-	V
Collector Emitter Saturation Voltage at $I_C = 10\text{ mA}$, $I_B = 1\text{ mA}$	$V_{CE(sat)}$	-	-	0.3	V
Transition Frequency at $V_{CE} = 10\text{ V}$, $-I_E = 5\text{ mA}$, $f = 100\text{ MHz}$	f_T	-	250	-	MHz
Input Resistance	R_1	7	10	13	K Ω



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MMDTC114TE

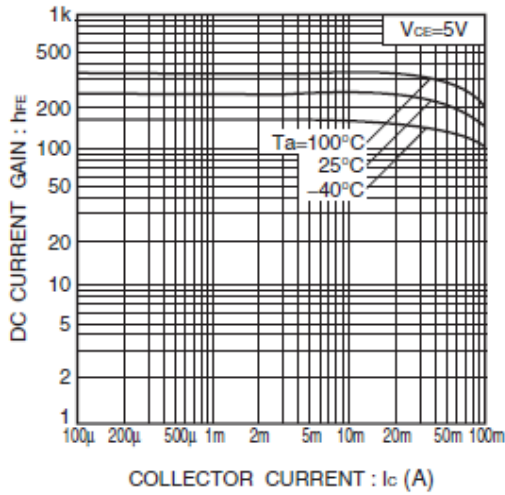


Fig.1 DC current gain vs. collector current

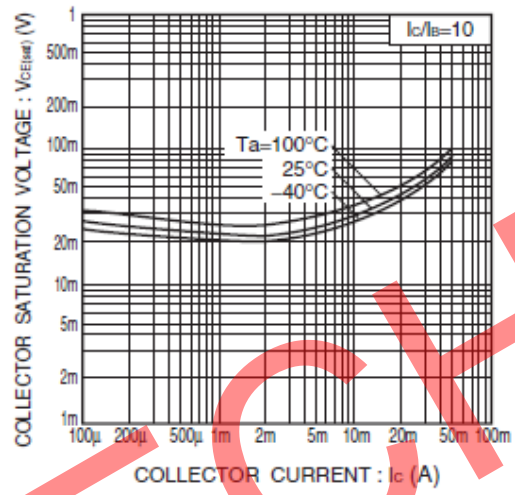
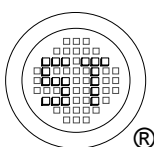


Fig.2 Collector-emitter saturation voltage vs. collector current



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