## **ST 2SA933**

## **PNP Silicon Epitaxial Planar Transistor**

for switching and AF amplifier applications.

The transistor is subdivided into three groups, O, Y and S, according to its DC current gain. As complementary type the NPN transistor ST 2SC945 is recommended.

On special request, these transistors can be manufactured in different pin configurations.



1. Emitter 2. Collector 3. Base TO-92 Plastic Package

Absolute Maximum Ratings (T<sub>a</sub> = 25 °C)

Parameter	Symbol	Value	Unit
Collector Base Voltage	-V <sub>CBO</sub>	50	V
Collector Emitter Voltage	-V <sub>CEO</sub>	40	V
Emitter Base Voltage	-V <sub>EBO</sub>	5	V
Collector Current	-I <sub>C</sub>	100	mA
Power Dissipation	P <sub>tot</sub>	300	mW
Junction Temperature	T <sub>j</sub>	150	°C
Storage Temperature Range	$T_{stg}$	- 55 to + 150	°C

## Characteristics at T<sub>2</sub> = 25 °C

Characteristics at T <sub>a</sub> = 25 °C					
Parameter	Symbol	Min.	Тур.	Max.	Unit
DC Current Gain at $-V_{CF} = 6 \text{ V}$ , $-I_{C} = 1 \text{ mA}$ Current Gain O	h <sub>FF</sub>	120	_	270	-
Group Y	h <sub>FE</sub>	180 270	-	390 560	-
	h <sub>FE</sub>	270	-	360	-
Collector Base Cutoff Current at -V <sub>CB</sub> = 30 V	-I <sub>CBO</sub>	-	-	0.5	μΑ
Emitter Base Cutoff Current at -V <sub>EB</sub> = 4 V	-I <sub>EBO</sub>	-	-	0.5	μA
Collector Base Breakdown Voltage at -I <sub>C</sub> = 50 μA	-V <sub>(BR)CBO</sub>	50	-	-	V
Collector Emitter Breakdown Voltage at -I <sub>C</sub> = 1 mA	-V <sub>(BR)CEO</sub>	40	-	-	V
Emitter Base Breakdown Voltage at $-I_E = 50 \mu A$	-V <sub>(BR)EBO</sub>	5	-	-	V
Collector Emitter Saturation Voltage at $-I_C = 50$ mA, $-I_B = 5$ mA	-V <sub>CE(sat)</sub>	-	-	0.5	V
Gain Bandwidth Product at $-V_{CE} = 12 \text{ V}$ , $-I_C = 2 \text{ mA}$	f⊤	-	140	-	MHz
Output Capacitance at $-V_{CB} = 12 \text{ V}$ , f = 1 MHz	C <sub>OB</sub>	-	-	5	pF

