

ESDBL6V8P

SILICON EPITAXIAL PLANAR DIODE

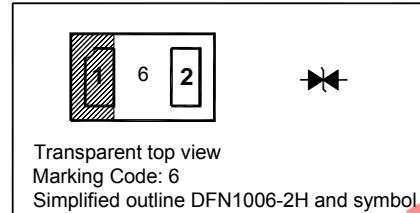
ESD protection diode

Features

- Ultra small mold type
- Bi-direction high reliability

PINNING

| PIN | DESCRIPTION |
|-----|-------------|
| 1 | Anode |
| 2 | Anode |

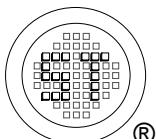
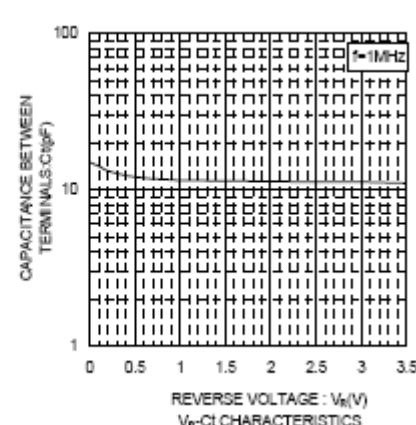
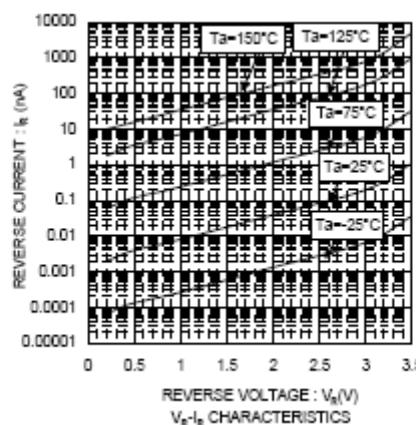
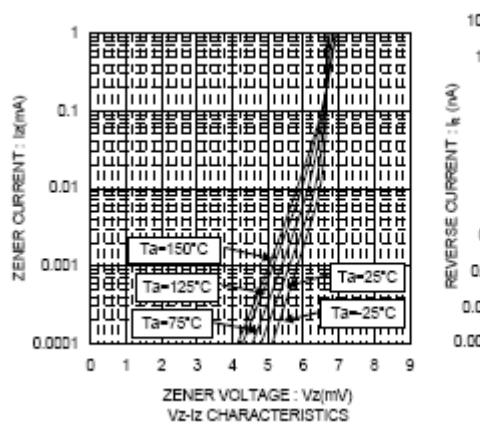


Absolute Maximum Ratings ($T_a = 25^\circ\text{C}$)

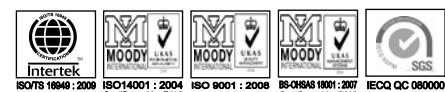
| Parameter | Symbol | Value | Unit |
|---|-----------|---------------|------------------|
| Peak Pulse Power ($t_p = 10 \times 1000 \mu\text{s}$) | P_{PK} | 10 | W |
| Power Dissipation | P_D | 100 | mW |
| Junction Temperature | T_J | 150 | $^\circ\text{C}$ |
| Storage Temperature Range | T_{stg} | - 55 to + 150 | $^\circ\text{C}$ |
| Operation Temperature Range | T_{opr} | - 55 to + 150 | $^\circ\text{C}$ |

Characteristics at $T_a = 25^\circ\text{C}$

| Parameter | Symbol | Min. | Typ. | Max. | Unit |
|--|--------|------|------|------|---------------|
| Zener Voltage at $I_z = 1 \text{ mA}$ | V_z | 5.78 | - | 7.82 | V |
| Reverse Current at $V_R = 3.5 \text{ V}$ | I_R | - | - | 0.5 | μA |
| Capacitance between Terminals at $V_R = 0 \text{ V}, f = 1 \text{ MHz}$ | C_T | - | 15 | - | pF |



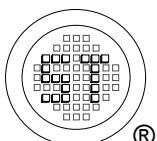
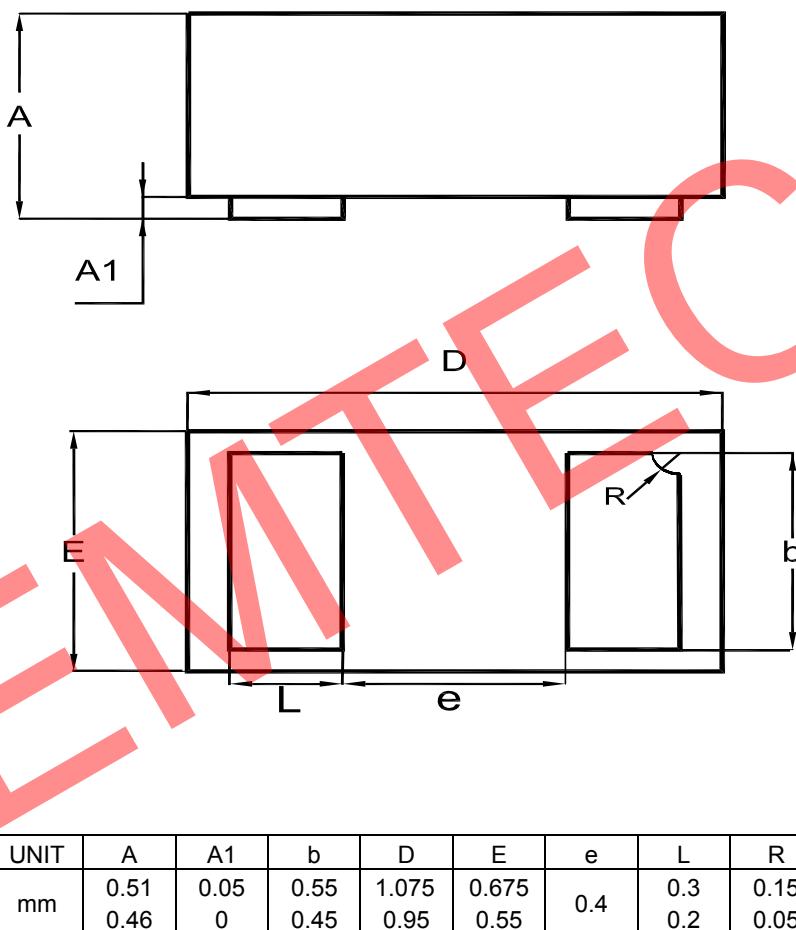
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PACKAGE OUTLINE

Plastic surface mounted package; 2 leads

DFN1006-2H



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