

ESDBL...P Series

Transient Voltage Suppressors

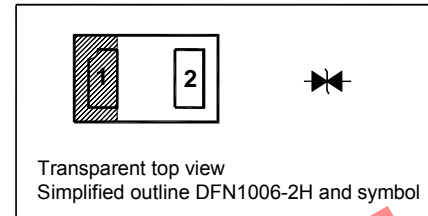
for ESD Protection

Features

- Low Leakage Current
- Bi-direction high reliability

PINNING

PIN	DESCRIPTION
1	Anode
2	Anode



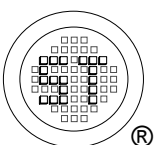
Absolute Maximum Ratings ($T_a = 25^\circ\text{C}$)

Parameter	Symbol	Value	Unit
IEC61000-4-2 (ESD) Air Contact	V_{ESD}	± 15 ± 8	KV
Peak Pulse Power On FR-5 Board ¹⁾	P_D	300	mW
Thermal Resistance Junction to Ambient Air	$R_{\theta\text{JA}}$	400	$^\circ\text{C/W}$
Junction Temperature	T_j	- 55 to + 150	$^\circ\text{C}$
Storage Temperature Range	T_{stg}	- 55 to + 150	$^\circ\text{C}$

¹⁾ FR-5 = 1.0 X 0.75 X 0.62 in .

Characteristics at $T_a = 25^\circ\text{C}$

Type	Marking Code	Reverse Stand-off Voltage	Reverse Current	Breakdown Voltage			Clamping Voltage	Reverse Peak Pulse Current	Capacitance		
		V_{RWM}	I_R at V_{RWM}	V_{BR}		at I_T	V_C at I_{PP}	I_{PP}	C_j	at f	at V_R
		Max. (V)	Max. (μA)	Min. (V)	Max. (V)	(mA)	Max. (V)	(A)	Typ. (pF)	(MHz)	(V)
ESDBL3V3P	C	3.3	1	5	7	1	10.5	1	15	1	0
ESDBL5V0P	E	5	1	5.8	7.8	1	12.5	1	15	1	0



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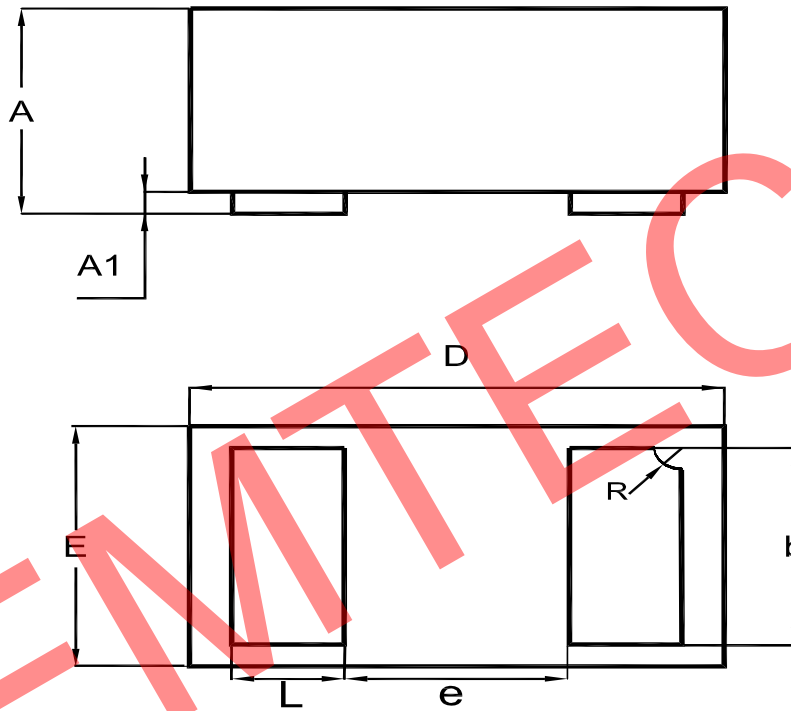


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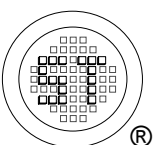
PACKAGE OUTLINE

Plastic surface mounted package; 2 leads

DFN1006-2H



UNIT	A	A1	b	D	E	e	L	R
mm	0.51	0.05	0.55	1.075	0.675	0.4	0.3	0.15
	0.46	0	0.45	0.95	0.55		0.2	0.05



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