

SD101AW...SD101CW

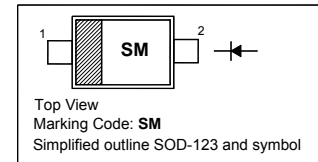
Surface Mount Schottky Barrier Diodes

Features

- Low forward voltage
- Low reverse capacitance

PINNING

PIN	DESCRIPTION
1	Cathode
2	Anode

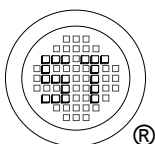


Absolute Maximum Ratings ($T_a = 25\text{ }^\circ\text{C}$)

Parameter	Symbol	Value	Unit		
Peak Repetitive Reverse Voltage	V_{RRM}	SD101AW SD101BW SD101CW	60 50 40	V	
Reverse Voltage		V_R	SD101AW SD101BW SD101CW	60 50 40	V
Forward Continuous Current			I_{FM}	15	mA
Power Dissipation	P_d		400	mW	
Non-Repetitive Peak Forward Surge Current	I_{FSM}	at $t = 1\text{ s}$ at $t = 10\text{ }\mu\text{s}$	50 2	mA A	
Operating and Storage Temperature Range			T_j, T_{stg}	- 65 to + 125	$^\circ\text{C}$

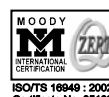
Characteristics at $T_a = 25\text{ }^\circ\text{C}$

Parameter	Symbol	Min.	Max.	Unit		
Reverse Breakdown Voltage at $I_R = 10\text{ }\mu\text{A}$	$V_{(BR)R}$	SD101AW SD101BW SD101CW	60 50 40	- - - V		
Forward Voltage at $I_F = 1\text{ mA}$		V_F	SD101AW SD101BW SD101CW	- - -	0.41 0.4 0.39 V	
at $I_F = 15\text{ mA}$			SD101AW SD101BW SD101CW	- - -	1 0.95 0.9	
Reverse Current at $V_R = 50\text{ V}$ at $V_R = 40\text{ V}$ at $V_R = 30\text{ V}$	I_R		SD101AW SD101BW SD101CW	- - -	200 200 200 nA	
Total Capacitance at $V_R = 0\text{ V}$, $f = 1\text{ MHz}$			C_T	SD101AW SD101BW SD101CW	- - -	2 2.1 2.2 pF
Reverse Recovery Time at $I_F = I_R = 5\text{ mA}$, $I_{rr} = 0.1X I_R$, $R_L = 100\text{ }\Omega$				t_{rr}	-	1



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ISO 9001:2008 Certificate No. 050008
ISO 14001:2004 Certificate No. 7116
BS-OHSAS 18001:2007 Certificate No. 7116
IECQ QC 080000 Certificate No. 050008

Dated : 30/10/2009

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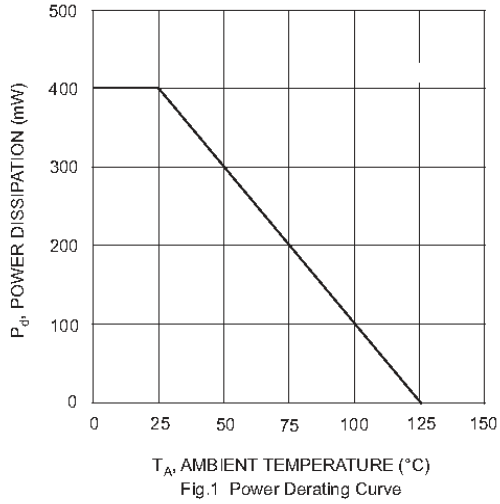


Fig. 1 Power Derating Curve

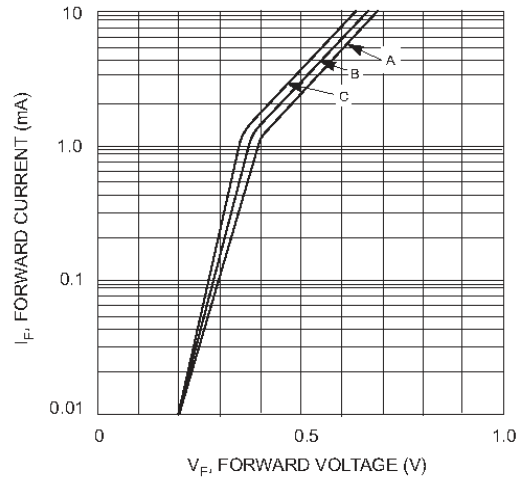


Fig. 2 Typical Forward Characteristic

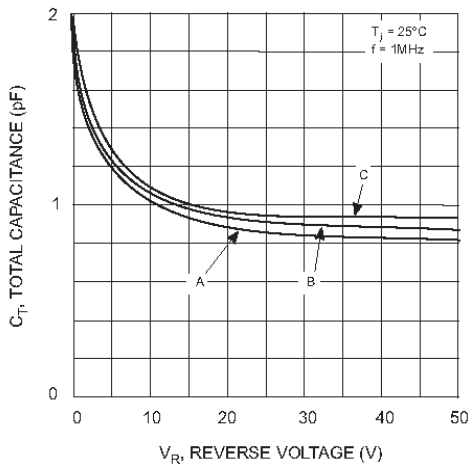


Fig. 3 Typical Total Capacitance vs Reverse Voltage

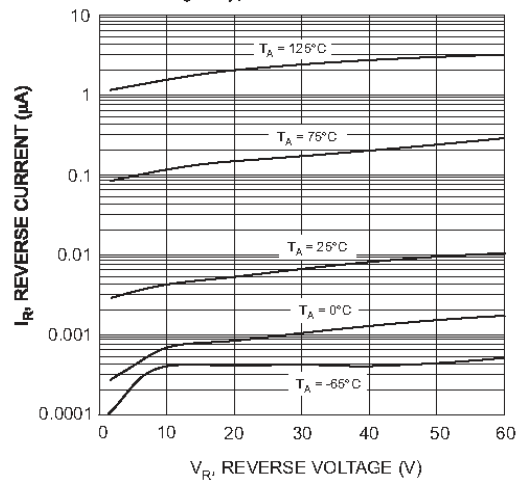
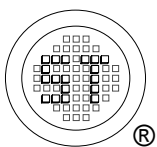


Fig. 4 Typical Reverse Characteristics



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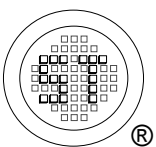
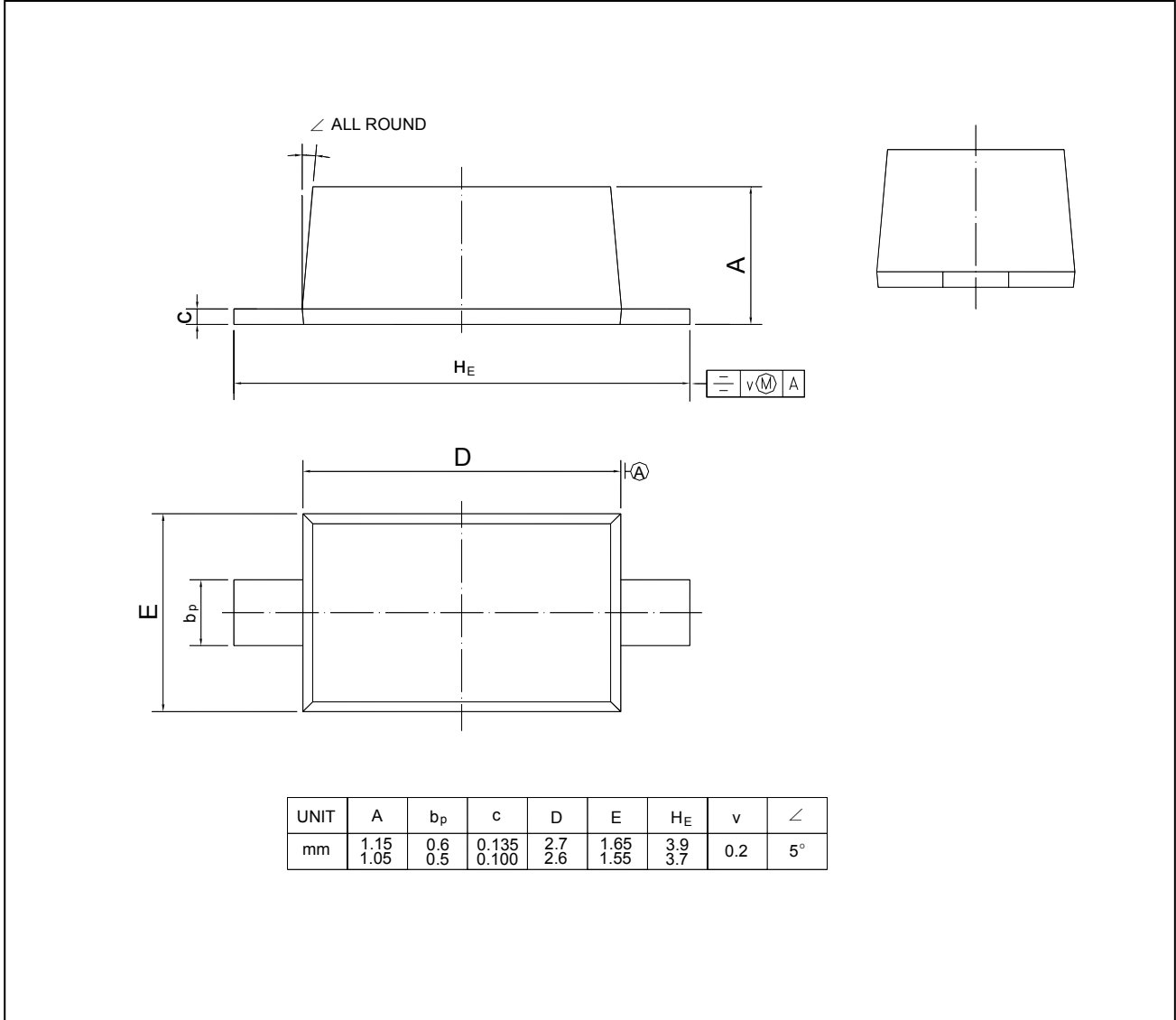


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PACKAGE OUTLINE

Plastic surface mounted package; 2 leads

SOD-123



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