

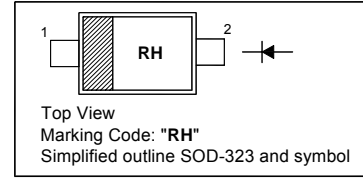
SDB720WS

SILICON EPITAXIAL PLANAR SCHOTTKY BARRIER DIODE

for high frequency rectification

PINNING

PIN	DESCRIPTION
1	Cathode
2	Anode

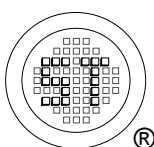
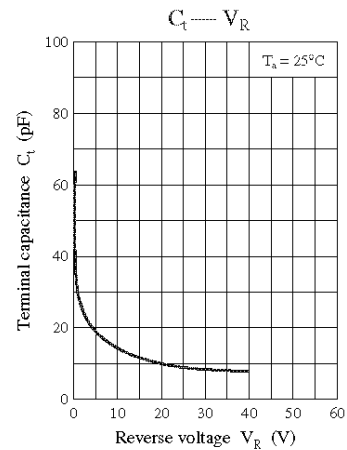
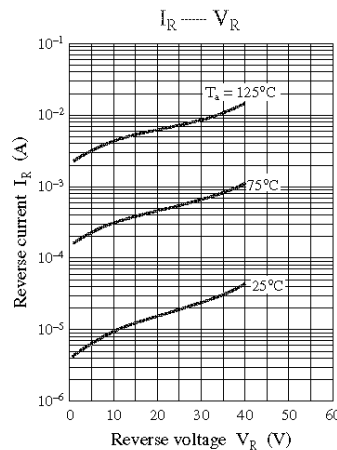
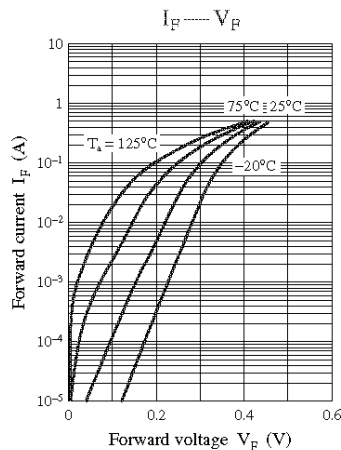


Absolute Maximum Ratings ($T_a = 25^\circ\text{C}$)

Parameter	Symbol	Value	Unit
Peak Reverse Voltage	V_{RM}	40	V
Reverse Voltage	V_R	40	V
Average Forward Current	I_O	0.5	A
Non-repetitive Peak Forward Surge Current	I_{FSM}	2	A
Junction Temperature	T_j	125	$^\circ\text{C}$
Storage Temperature Range	T_{stg}	- 55 to + 150	$^\circ\text{C}$

Characteristics at $T_a = 25^\circ\text{C}$

Parameter	Symbol	Typ.	Max.	Unit
Forward Voltage at $I_F = 500\text{ mA}$	V_F	-	0.55	V
Reverse Current at $V_R = 35\text{ V}$	I_R	-	100	μA
Terminal Capacitance at $V_R = 0\text{ V}$, $f = 1\text{ MHz}$	C_T	60	-	pF
Reverse Recovery Time at $I_F = I_R = 100\text{ mA}$, $I_{rr} = 10\text{ mA}$, $R_L = 100\ \Omega$	t_{rr}	5	-	nS



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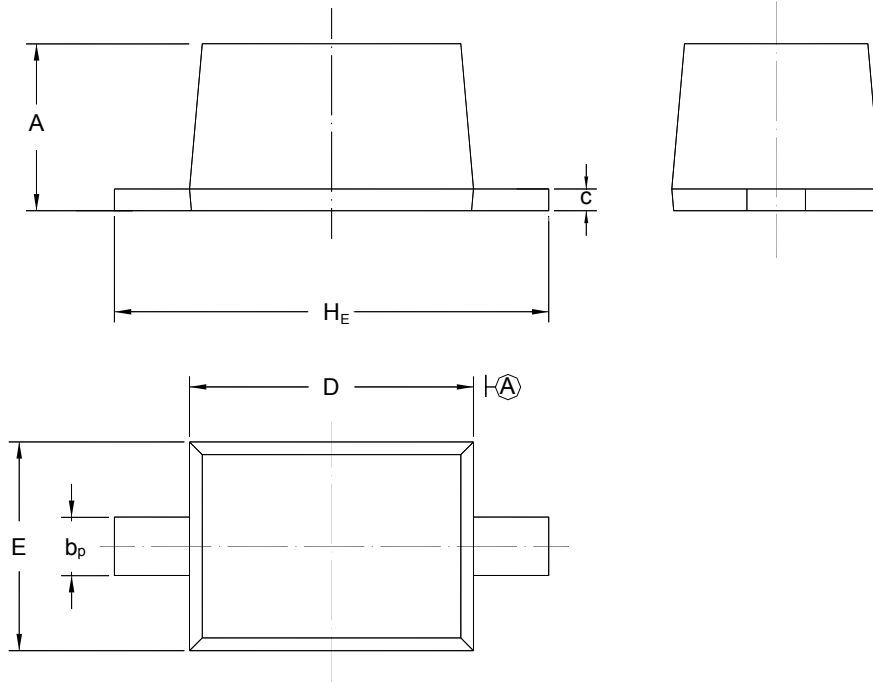
Dated : 16/01/2007

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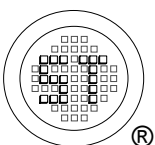
PACKAGE OUTLINE

Plastic surface mounted package; 2 leads

SOD-323



UNIT	A	b _p	C	D	E	H _E
mm	1.10 0.80	0.40 0.25	0.15 0.10	1.80 1.60	1.35 1.15	2.80 2.30



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